

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Page 1 of 2



Filing Date

February 14, 2002

First Named Inventor

Baude, Paul F.

Art Unit

2812

Examiner Name

Attorney Case Number

57181US002

U.S. Patent Documents

Exam. Init.*	Cite No.	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Doc. Number-(Kind Code if Known)			
WZ	A1	US- 3,669,060	06/13/72	Page et al.	
	A2	US- 4,096,821	06/27/78	Greeneich et al.	
	A3	US- 4,335,161	06/15/82	Luo	
	A4	US- 4,676,193	06/30/87	Martin	
	A5	US- 4,746,548	05/24/88	Boudreau et al.	
	A6	US- 4,915,057	04/10/90	Boudreau et al.	
	A7	US- 4,963,921	10/16/90	Kariya et al.	
	A8	US- 5,397,607	03/14/95	Shields et al.	
	A9	US- 6,087,196	07/11/00	Sturm et al.	
	A10	US- 6,106,627	08/22/00	Yializis	
	A11	US- 6,285,001 B1	09/04/01	Fleming et al.	

Foreign Patent Documents

Exam. Init.*	Cite No.	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation (Check if yes)
		Ctry. Code	Number-KindCode (if known)				
	B1	EP	0 118 576	12/02/87			
	B2	EP	0719 638 A2	07/03/96			
	B3	JP	5-228669	09/07/93			X
	B4	JP	9-27454	01/28/97			X
	B5	PCT	WO 99/30336	06/17/99			
	B6	PCT	WO 99/39373	08/05/99			
	B7	PCT	WO 99/54786	10/28/99			

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Exam. Init.*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	C1	Brody, "Large Area Masking Techniques for Thin Film Transistor Arrays," SPIE, Vol. 100, Semiconductor Microlithography II, pp. 140-150(1977)
	C2	Weimer, "The TFT- A New Thin-Film Transistor," Proceedings of the IRE, pp. 1462-1469(1962).
	C3	Brody et al., "Flexible Thin-Film Transistors Stretch Performance, Shrink Cost," Research, pp. 20-23 (1969).

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Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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WJ	C4	Schon et al., "Fast Organic Electronic Circuits Based on Ambipolar Pentacene Field-Effect Transistors," Applied Physics Letters, Vol. 79, No. 24, pp. 4043-4044 (2001),
	C5	Gouy et al., "Deposition of Organic Semiconductor Through Silicon Shadow Masks for Integrated Led on Silicon."
	C6	Kim et al., "Photoplastic Shadow-Masks for Rapid Resistless Multi-Layer Micropatterning".
	C7	Kim et al., "Dry-Lift-Off Patterning on Fragile Surfaces Using MEMS Shadow Masks," pp. 619-622.

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Application Number

076003

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	A4	US-			
	A5	US-			
	A6	US-			
	A7	US-			
	A8	US-			
	A9	US-			
	A10	US-			
	A11	US-			

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<i>WJ</i>	B1	PCT	WO99/09456	02/25/99			
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	B3						
	B4						
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	C2	
	C3	

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<i>W</i>	A1	US- 2002/0062785 <i>A1</i>	05/30/2002	Kim et al.	
	A2	US-			
	A3	US-			
	A4	US-			
	A5	US-			
	A6	US-			
	A7	US-			
	A8	US-			
	A9	US-			
	A10	US-			
	A11	US-			

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